

Fig. 2

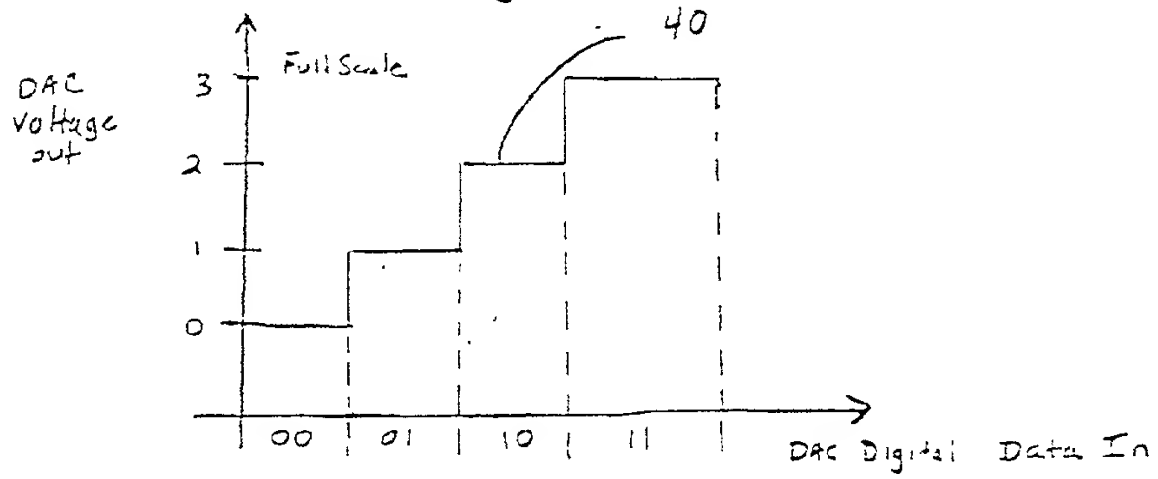


Fig. 3

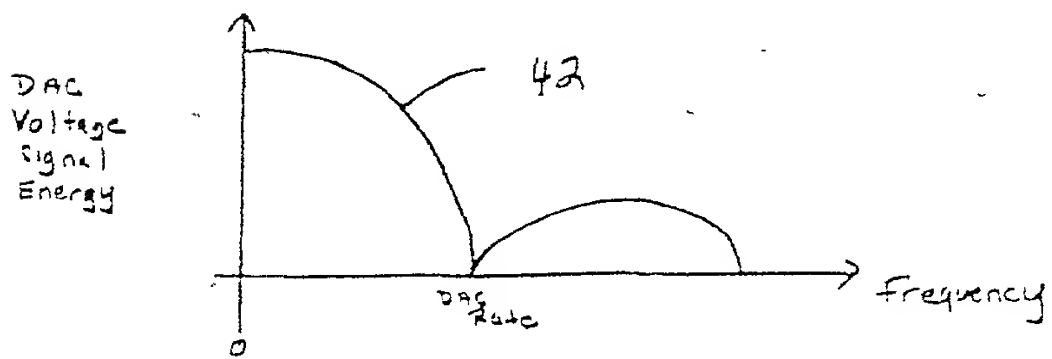


Fig. 4

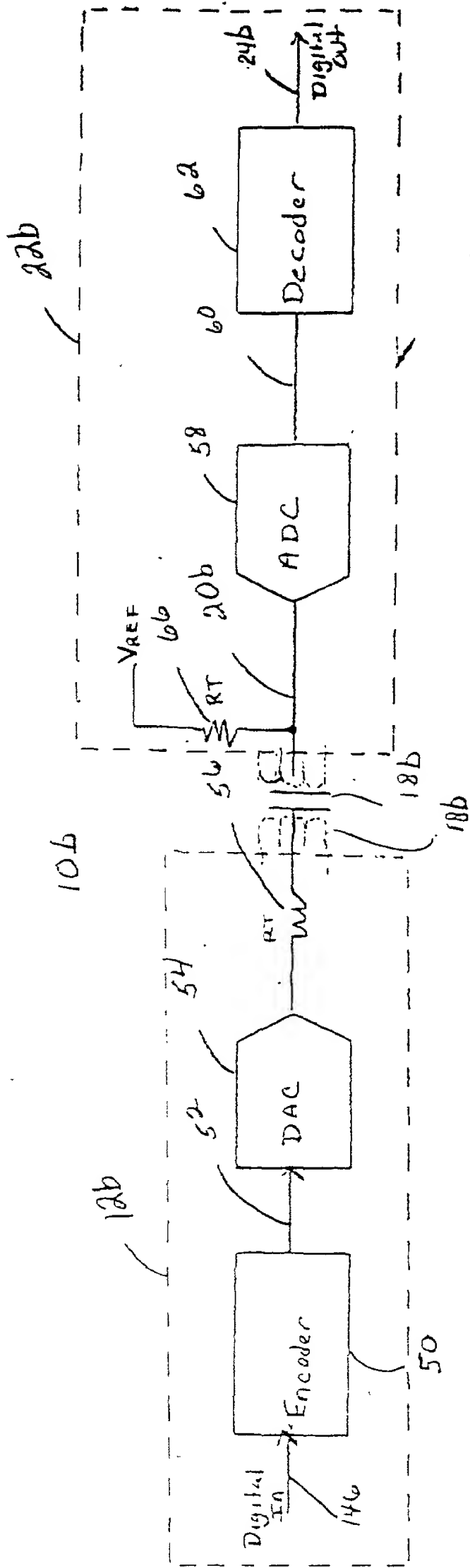


Fig. 5

ENCODER

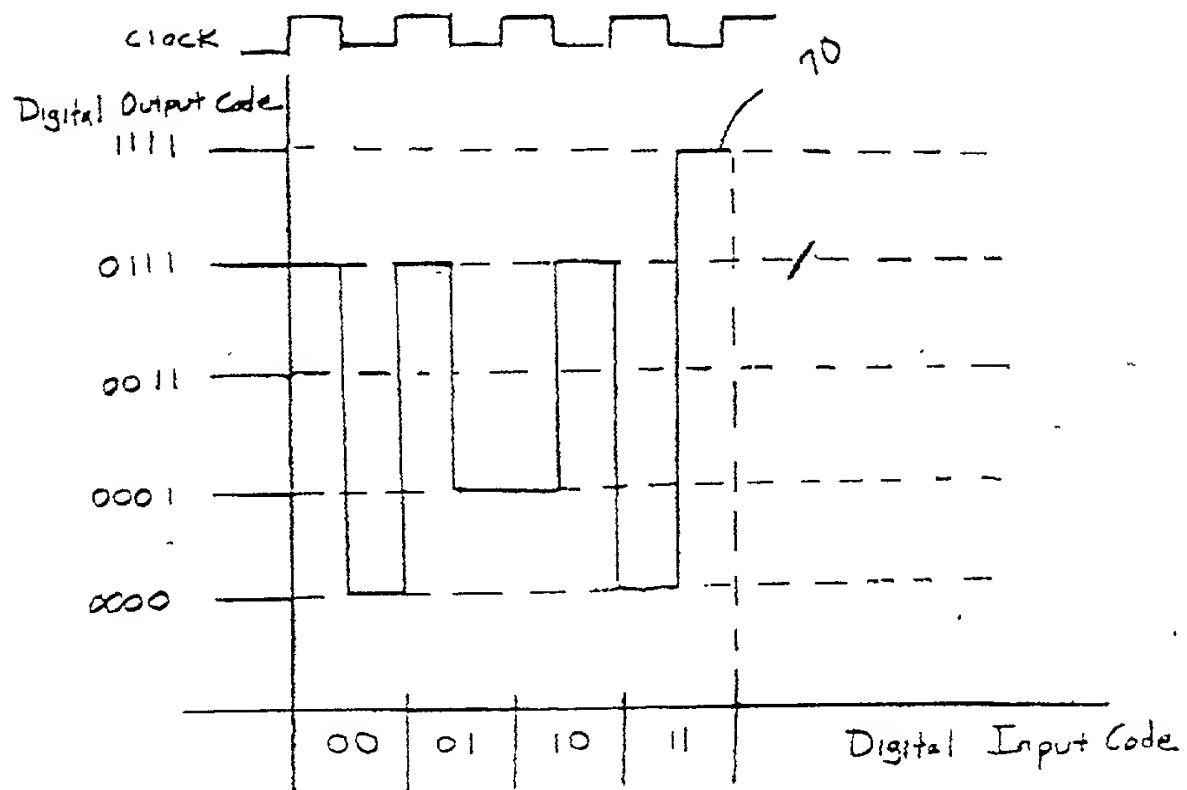


Fig. 6

DAC

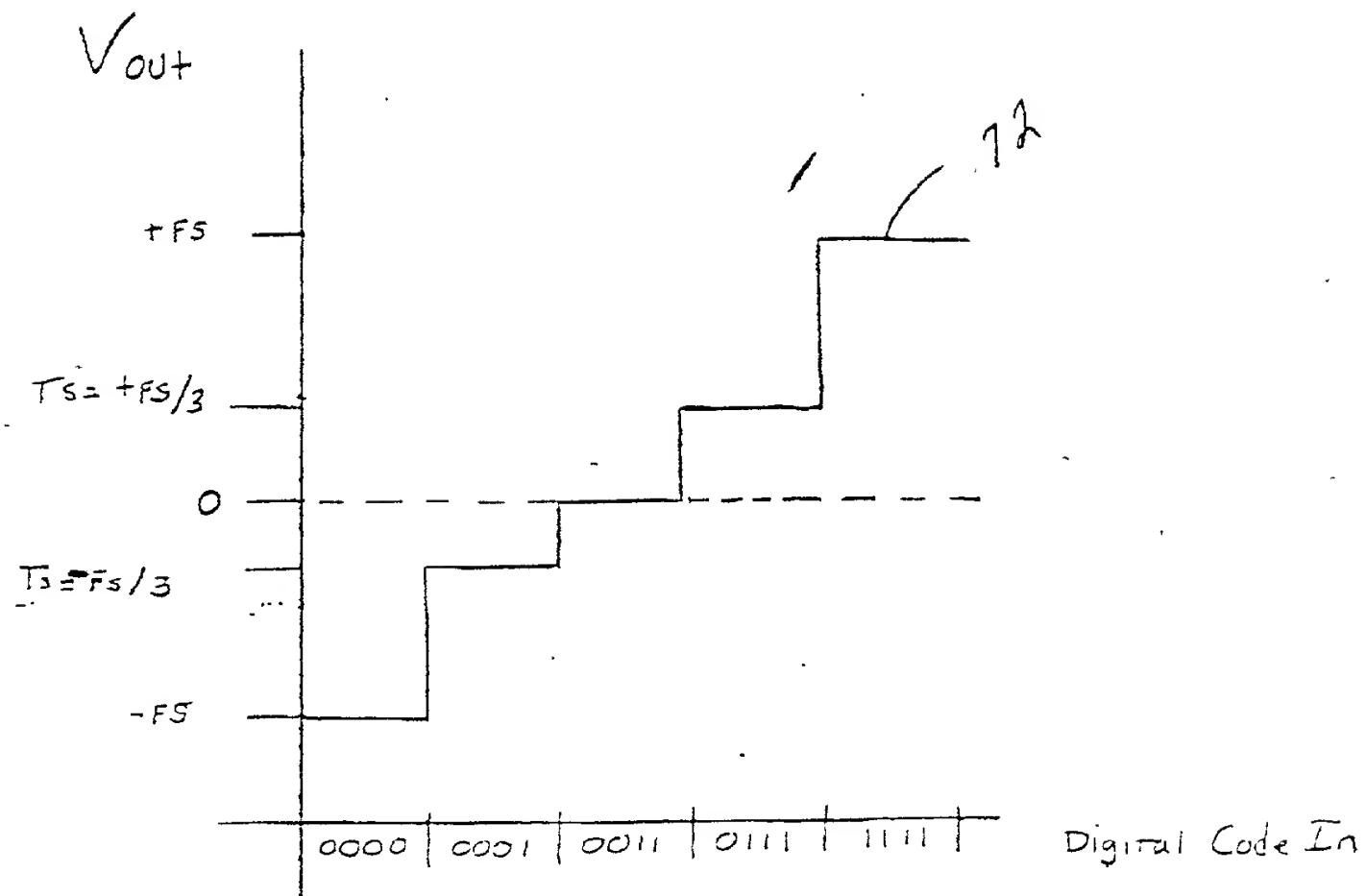
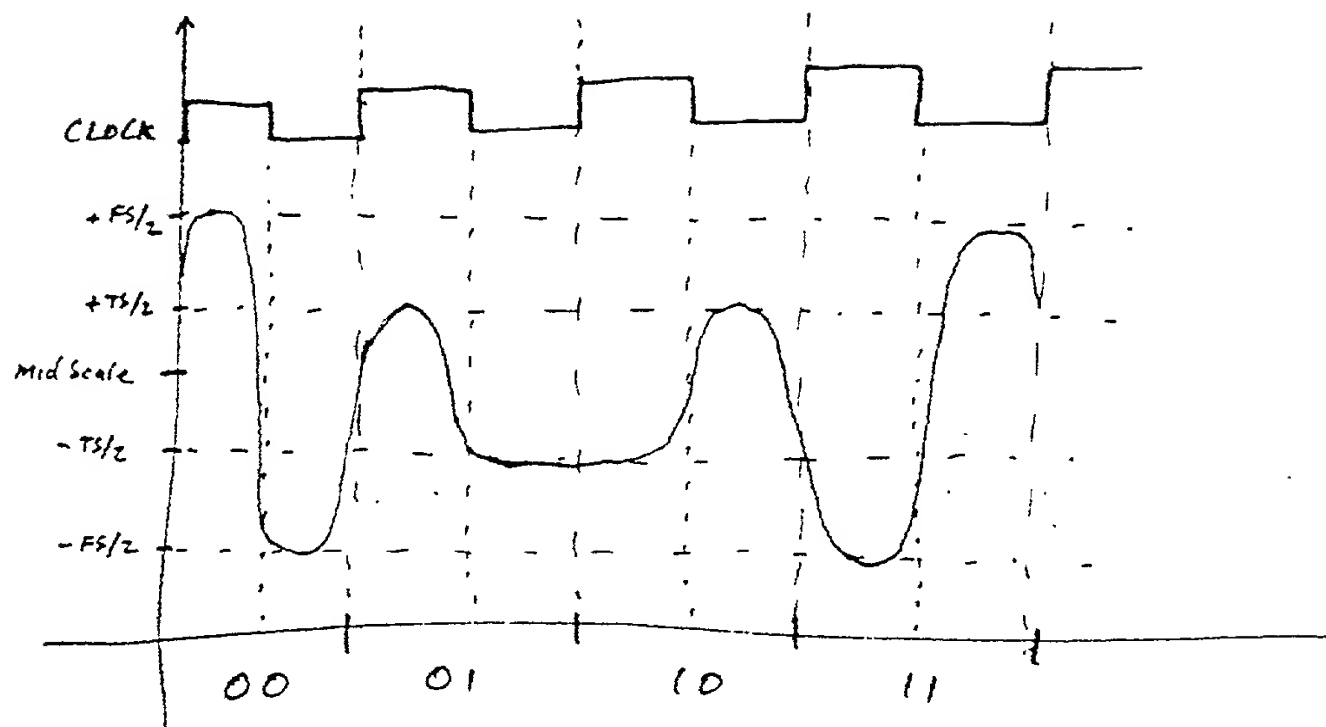


Fig. 7

Fig. 8A Barrier Pin Signal



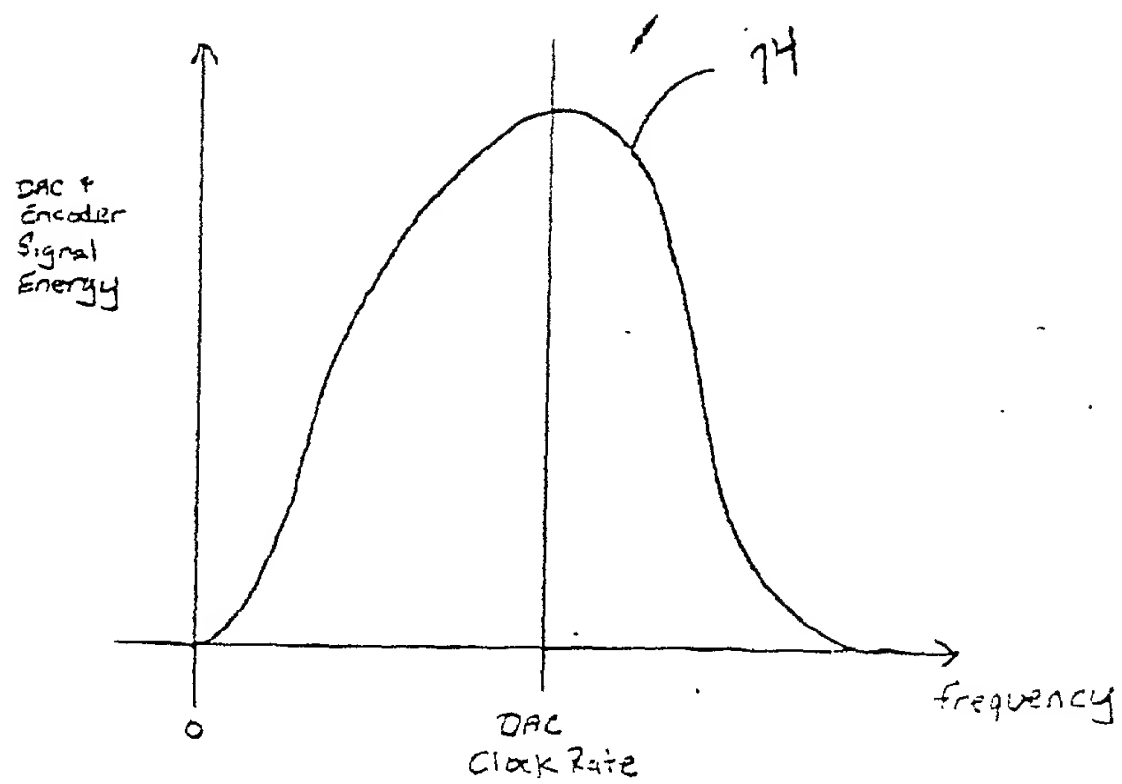


Fig. 8B

applicable data from said system, and
the data from the system is then sent
to the data bus.

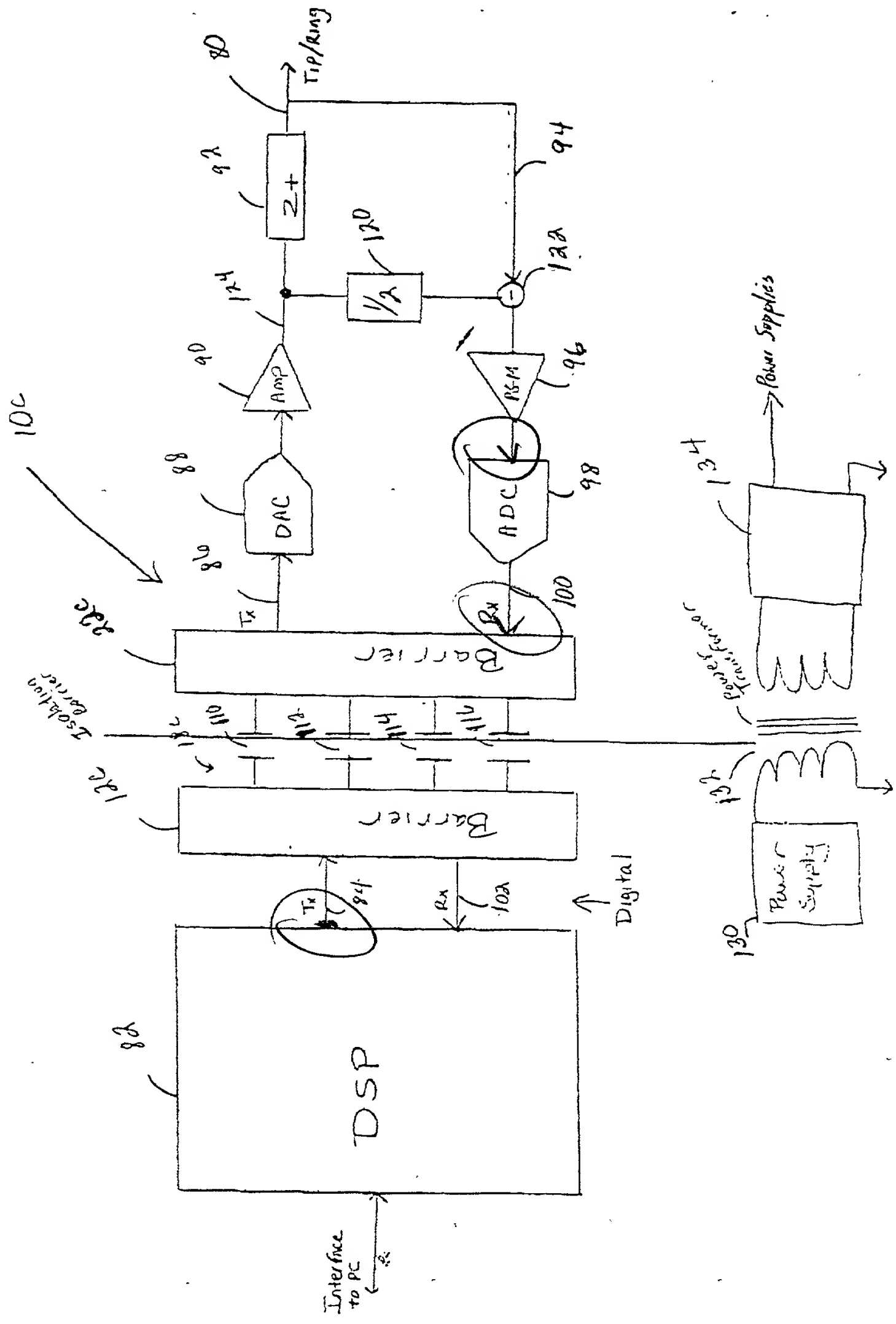


Fig. 9

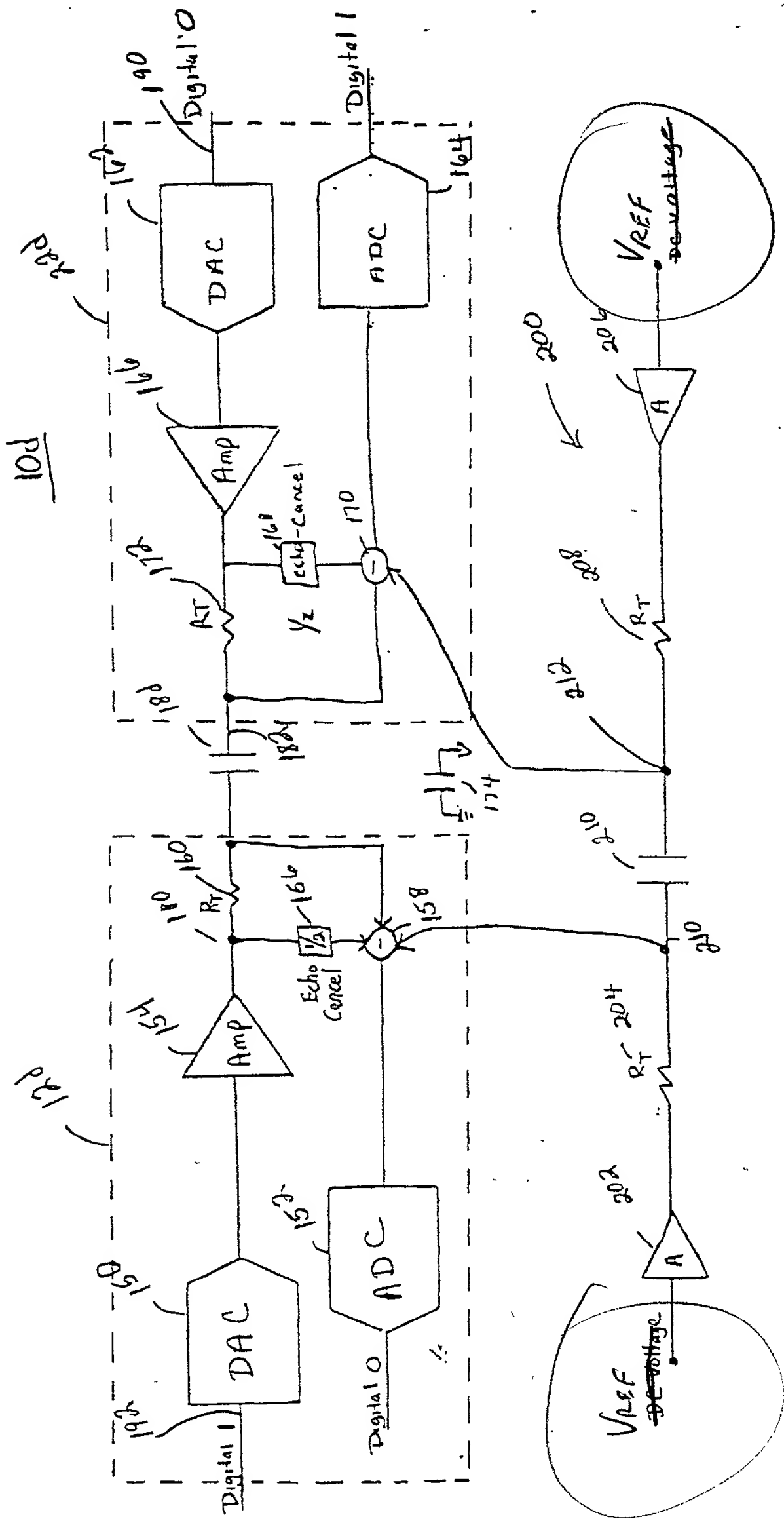


Fig. 10

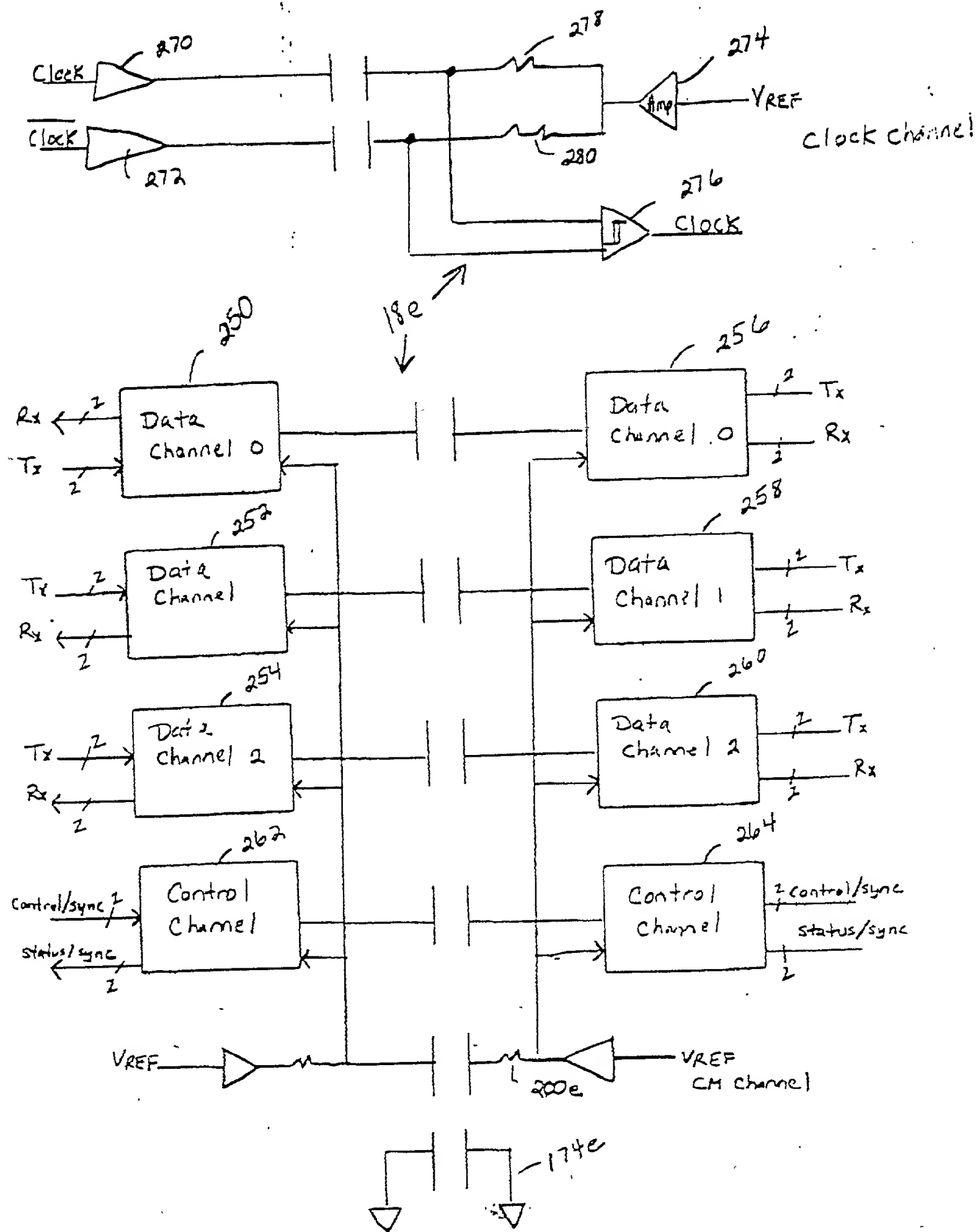


Fig. 11

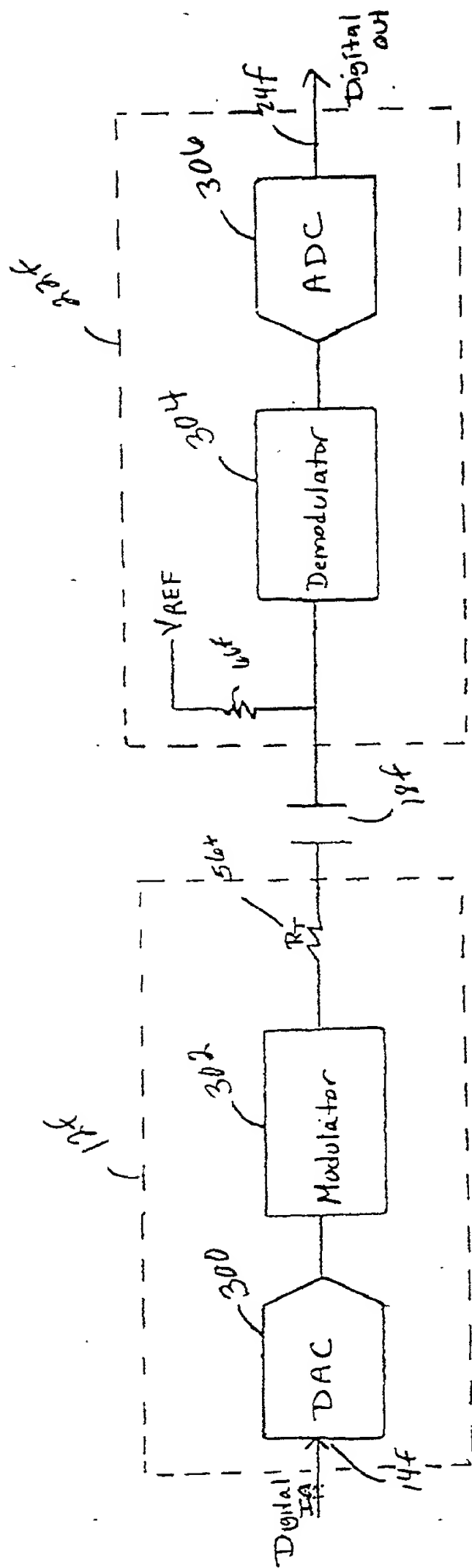


Fig. 12